

100	*= \$600	
110	LDA \$D300	Get 80 column flag bit from JOY Port#2.
120	ASL A	Move it over to the toggle bit position.
130	AND #\$80	Extract only it, and discard other bits
140	EOR #\$80	and then invert it for later use in
150	;	changing the toggle bit direction.
160	;	
170	;	===== USE DDR TO SET BIT DIRECTION =====
180	LDX #\$38	
190	STX \$D302	Use PACTL register to switch to DDR.
200	STA \$D300	ACC holds toggle bit Direction.
210	LDX #\$3C	
220	STX \$D302	Switch PORTA back to I/O.
230	;	
240	;	If the 80 column flag was set, then no
250	;	change would happen to port direction
260	;	(it remains an input), thus no toggling.
270	;	
280	;	===== TOGGLE F/F CLOCK LINE =====
290	LDA #0	Bring Toggle Bit (PORTA-7) low
300	STA \$D300	
310	NOP	
320	LDA #\$80	Bring Toggle Bit high (F/F clocked).
330	STA \$D300	
340	;	===== RESET PORT TO ALL INPUTS =====
350	LDX #\$38	
360	STX \$D302	Use PACTL register to switch to DDR.
370	LDA #0	
380	STA \$D300	Set PORTA to all inputs.
390	LDX #\$3C	
400	STX \$D302	Switch PORTA back to I/O.
410	LDY #1	Set for hand-off to DOS.
420	RTS	